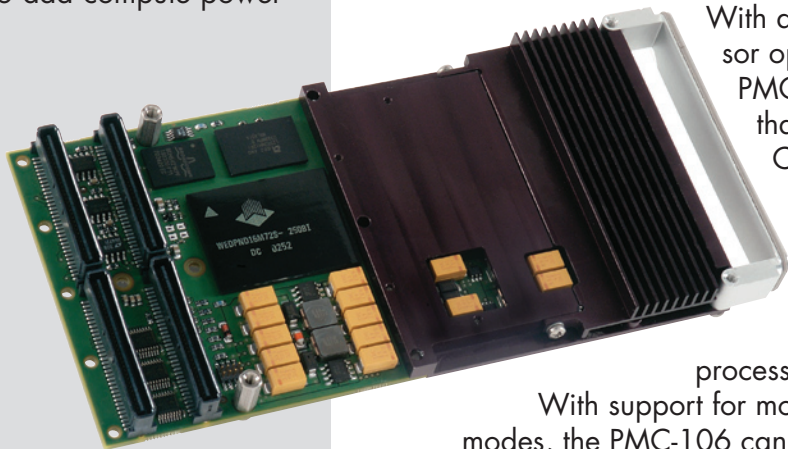




PMC-106

PMC processors. The open-architecture COTS alternative to add compute power to custom boards.

The PMC-106 is a small single board computer (SBC) provided in the PMC form-factor. The PMC-106 offers a standard open-architecture solution for applications that cannot be addressed with VME or CompactPCI technologies. The processor PMC standard provides a convenient, modular and low-risk alternative approach to incorporating processing capability into an embedded system.



With a PowerPC 7447/7448 processor operating at up to 1GHz, the PMC-106 packs a processing punch that belies its small physical size. Combined with high bandwidth double data rate SDRAM, a 133MHz PCI-X (1GByte/sec peak) interface and a Gigabit Ethernet port the PMC-106 is able to tackle demanding processing and I/O requirements.

With support for monarch and non-monarch PCI modes, the PMC-106 can be used as the processor on non-intelligent I/O cards, or as an auxiliary processor on intelligent cards. The PMC-106 is designed to fit a range of thermal environments including rugged air-cooled and conduction-cooled applications.

For more information on our broad range of high-integrity computing solutions, please visit our website at www.cwembedded.com.

**CURTISS
WRIGHT** Controls
Embedded Computing



PMC-106

Features

- ◆ FreeScale PowerPC 7447448 processor up to 1.0 GHz
- ◆ VITA-32 PPMC compliant, supporting Monarch and Non-Monarch modes
- ◆ VITA-39 PMC-X capable 64-bit interface up to 133 MHz
- ◆ Up to 512 Mbytes ECC protected DDR-250 SDRAM
- ◆ 64 Mbytes Flash memory
- ◆ 8 Kbytes ferroelectric memory (FeRAM)
- ◆ One 10/100/1000Base-T Ethernet Port
- ◆ Supports Universal PCI signaling
- ◆ Four channel DMA controller
- ◆ Two EIA-232/422 asynchronous serial ports
- ◆ Six discrete LVTTTL I/O signals
- ◆ Two temperature sensors
- ◆ Four 32-bit counter/timers
- ◆ Two independent JTAG COP emulator interfaces
- ◆ VxWorks® real-time operating system
- ◆ Linux from Curtiss-Wright
- ◆ SSSL optimized AltiVec DSP function library
- ◆ Range of conduction-cooled ruggedization levels available

